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P. 09

Customer No.: 31561 Application No.: 10/707,664

Docket NO.: 7635-US-PA

In The Specification:

Please amend paragraph [0030] as follows:

[0030] In this embodiment, the first thin film transistor 102 and its electrically

connected reflective electrode 108 and the second thin film transistor 102 104 and its

electrically connected light-emitting diode 118 can be positioned inside two separate

pixel regions or inside a single pixel region. In other words, in one preferred embodiment,

the first thin film transistor 102 and its electrically connected reflective electrode 108 are

formed in a first pixel region, and the second thin film transistor 102 104 and its

electrically connected light-emitting diode 118 are formed in a second pixel region. In

another embodiment, the first thin film transistor 102, the reflective electrode 108

electrically connected to the first thin film transistor 102, the second thin film transistor

403 104 and light-emitting diode 118 electrically connected to the second thin film

transistor 402 104 are all formed in the one pixel region. In addition, two or more thin

film transistors may be used to drive the light-emitting diode 118 all according to the

design requirement.

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